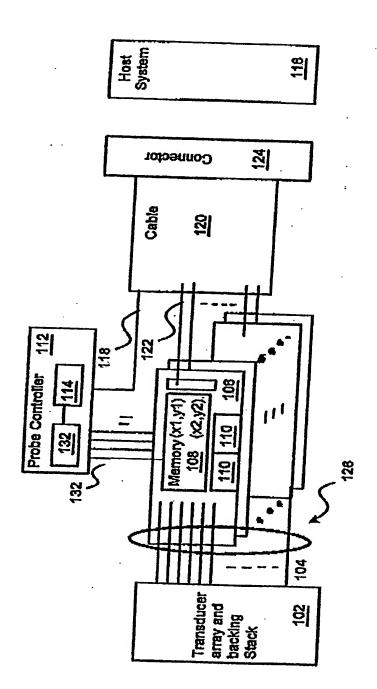
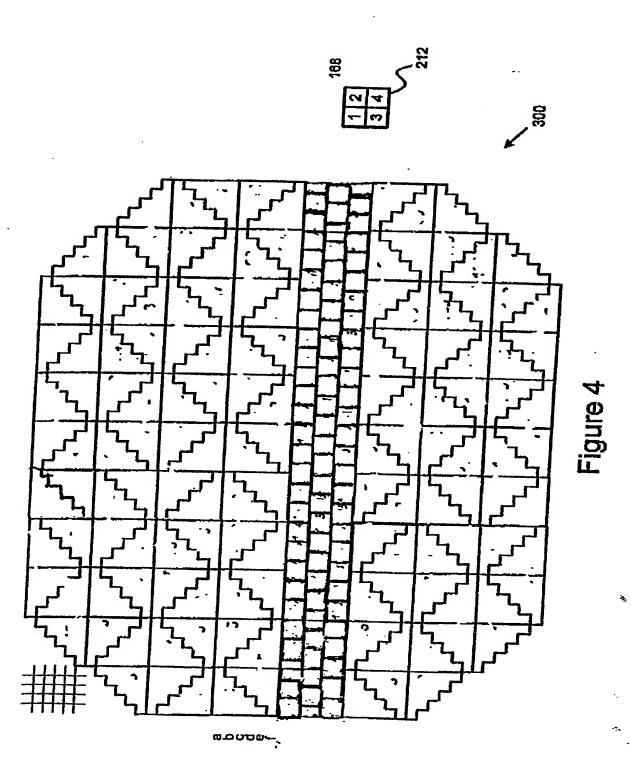
Figure 1



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正

Figure 2



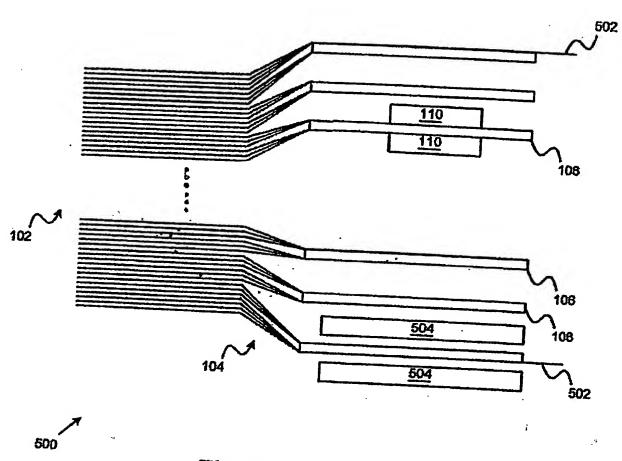
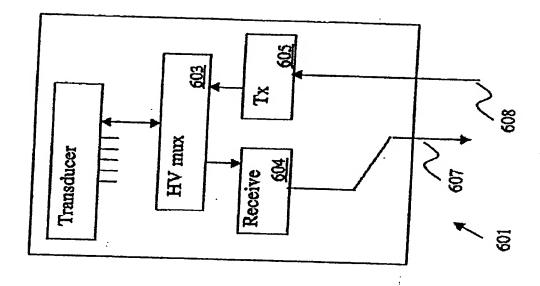


Figure 5



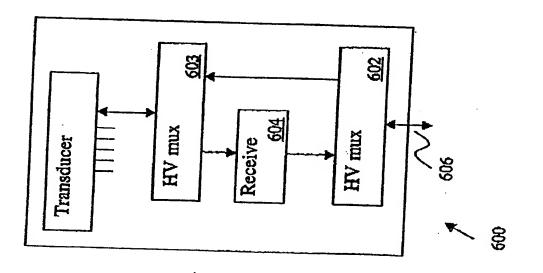
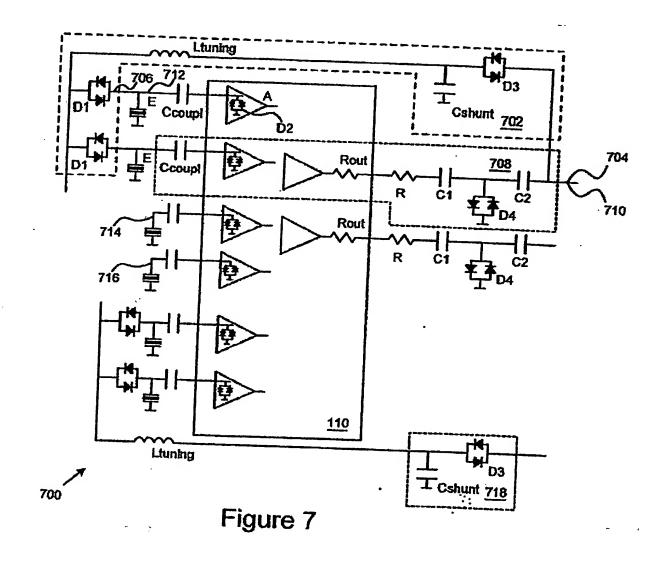


Figure 6



D3 and D4 Diode Arrays

Signal Processors

110

D1 Diode Arrays

Transducer Flex: (annector)

D3 and D4 Diode Arrays

Signal Processors

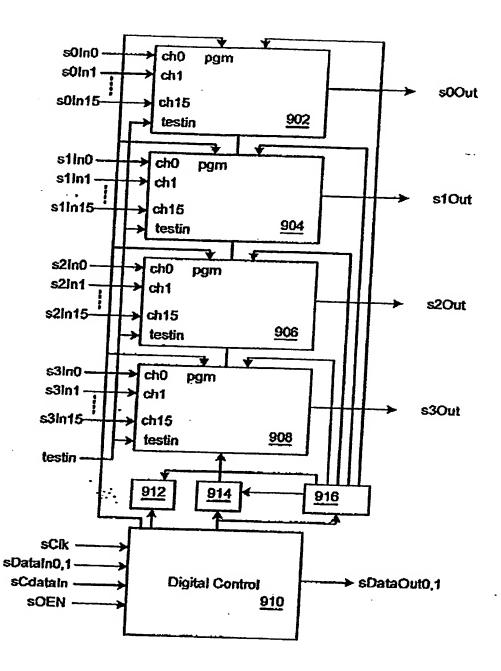
110

D1 Diode Arrays

Transducer Flex Connector

804

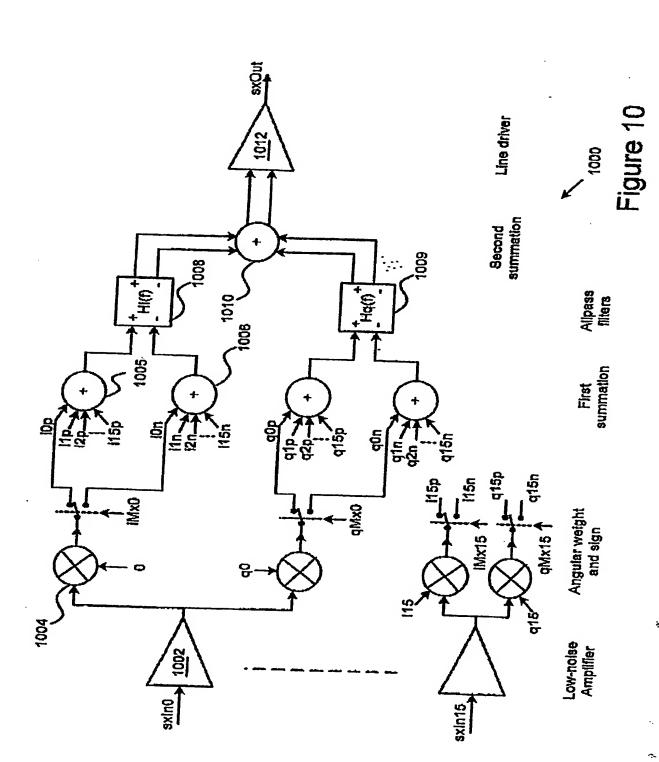
Figure 8



· 1



Figure 9



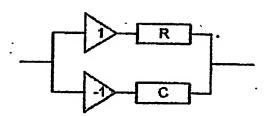
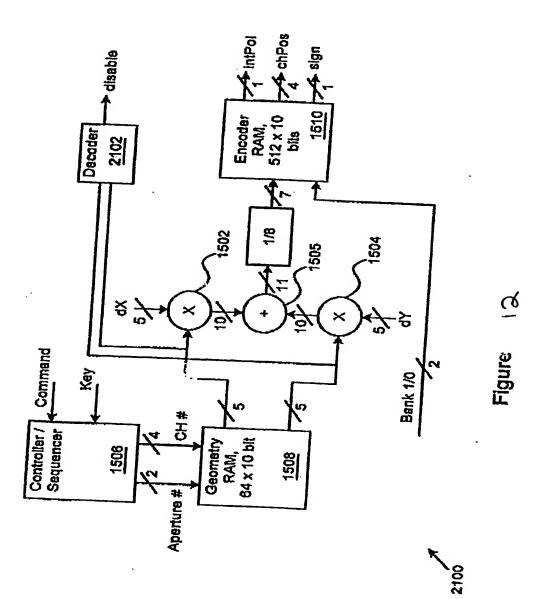
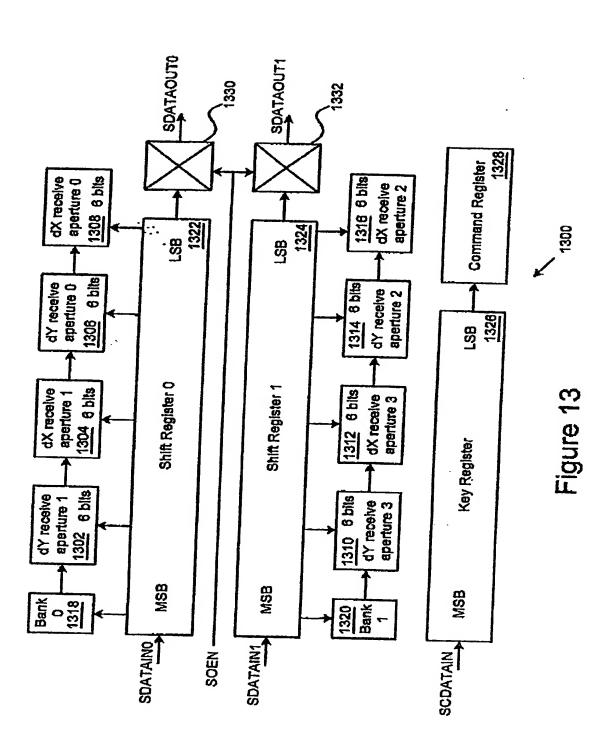


Figure \\





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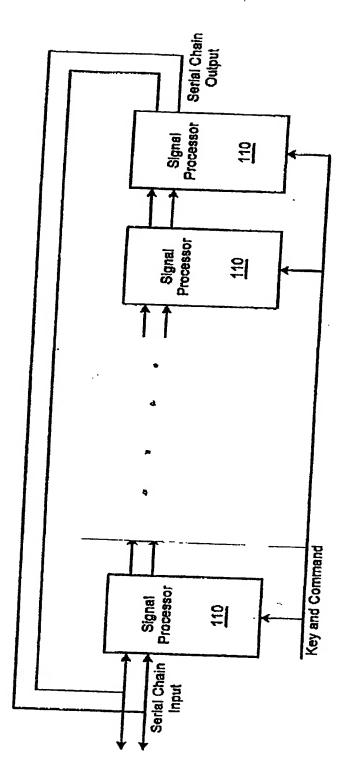


Figure 14

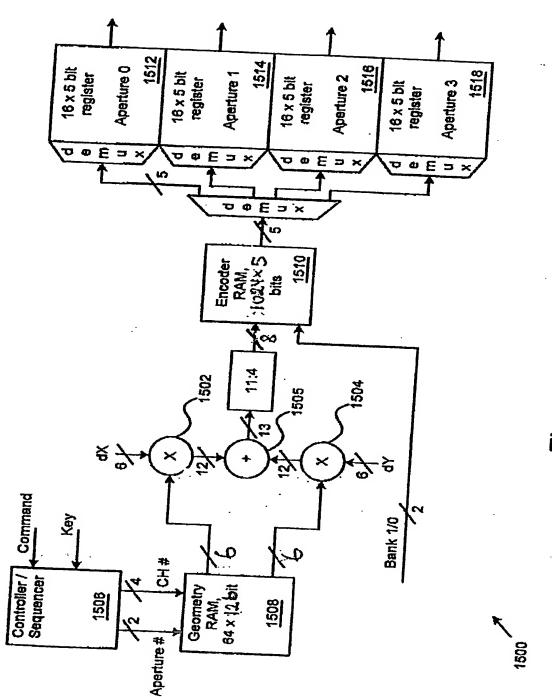


Figure 15

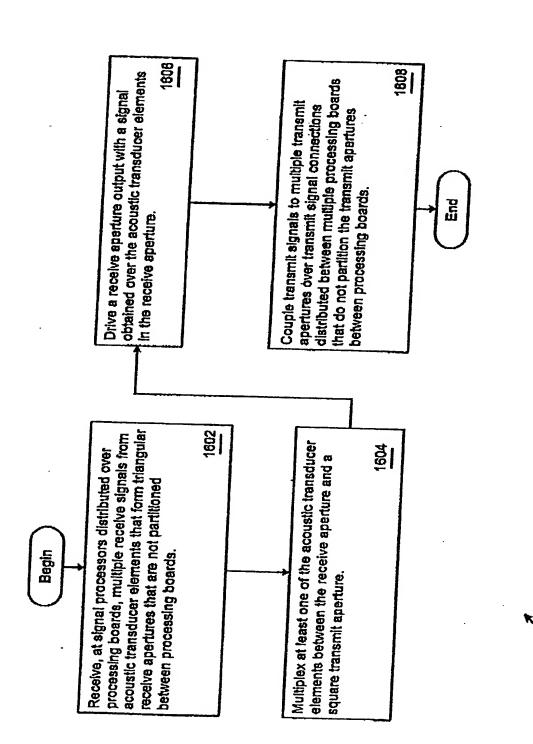


Figure 16

## FIGURE 17 Begin Receive directional parameters for received sub-apertures from a host system at cache memory controller. Transfer the directional parameters to multiple signal processors on multiple processing boards. 1704 Couple, to a first signal processor, receive signals arising form a receive sub-aperture. 1706 Retrieve, from a cache memory, directional parameters for the receive sub-aperture. 1708 Determine a beamforming delay derived from the directional parameters for the transducer elements in the receive subaperture. 1710 Apply the delay to the receive signal from teach respective transducer element. 1712 End

Couples a transmit pulse through a transmit section input, a transmit section output, and receive signal blocking circuitry coupled between the transmit section input and the transmit section output.

1802

Couple a receive signal through a receive section input, a receive section output, and transmit signal blocking circuitry coupled between the receive section input and the receive section output.

1804

Figure 18

Figure 19